

## Contents

This is the chisel intro for the TDT4255 course at NTNU. If you use it for your course, please let me know how it went and feel free (or even slightly pressured) to submit pull requests with improvements.

In order to get started you should go through the introductory material:

- Introduction to hardware description languages
- Introduction to hardware chisel

The deliverable for this exercise is constructing a matrix multiplying unit:

- Matrix multiplier exercise

The main course(work) for creating a 5-stage RISC-V pipeline can be found here:

- [https://github.com/PeterAaser/TDT4255\\_EX2](https://github.com/PeterAaser/TDT4255_EX2)